## SKHI 61 (R) ...



## Sixpack IGBT and MOSFET Driver

**SKHI 61 (R)** 

## **Features**

- CMOS-compatible input buffers at V<sub>DD</sub>=5V
- Short-circuit protection by V<sub>CE</sub>-monitoring and Soft-turn-Off
- Drive interlock top/bottom
- Signal transmission by opto-couplers
- Supply undervoltage protection (13V)
- Error latch / output

## **Typical Applications**

- Driver for IGBT and MOSFET modules in three-phase-bridge circuits, inverter drives, UPS-facilities, etc.
- At T<sub>a</sub> < -25°C the current consumption can be 1,6 times the rated maximum current for the first three operating minutes.

Absolute Maximum Ratings						
Symbol	Conditions	Values	Units			
$V_S$	Supply voltage primary	15,6	V			
$V_{iH}$	Input signal voltage	V <sub>S</sub> + 0,3	V			
Iout <sub>PEAK</sub>	Output peak current	2	Α			
lout <sub>AVmax</sub>	Output average current (T <sub>a</sub> = 85 °C)	20	mA			
f <sub>max</sub>	Max. switching frequency (C <sub>GE</sub> < 9nF)	50	kHz			
$V_{CE}$	Collector emitter voltage sense across	900	V			
	the IGBT (for 1200V-IGBTs)					
dv/dt	Rate of rise and fall of voltage	15	kV/μs			
	(secondary to primary side)					
$V_{\text{isolIO}}$	Isolation test voltage input - output	2500	V			
	(2 sec. AC)					
V <sub>isol12</sub>	Isolation test voltage output 1 - output 2	1500	V			
	(2 sec. AC)					
$R_{Gonmin}$	Minimum rating of R <sub>Gon</sub>	10	Ω			
$R_{Goffmin}$	Minimum rating for R <sub>Goff</sub>	10	Ω			
Q <sub>out/pulse</sub>	Max. rating for gate T <sub>a</sub> = 85 °C	0,7	μC			
	charge per pulse T <sub>a</sub> = 55 °C	1	μC			
T <sub>op</sub>	Operating temperature	- 40 <b>+</b> 85	°C			
T <sub>stg</sub>	Storage temperature	- 40 <b>+</b> 85	°C			

<b>Characteristics</b> $T_a = 25$ °C, unless otherwise specifie					
Symbol	Conditions	min.	typ.	max.	Units
V <sub>s</sub>	Supply voltage primary	14,4	15,0	15,6	V
I <sub>SO</sub> 1)	Supply current no load	160		200	mA
	primary side normal op.			450	mA
$V_{iT+}$	Input threshold voltage (High)	4,0			V
V <sub>iT-</sub>	Input threshold voltage (LOW)			1,5	V
R <sub>in</sub>	Input resistance		60		kΩ
$V_{G(on)}$	Turn on gate voltage output		14,9		V
$V_{G(off)}$	Turn off gate voltage output		-6,5		V
R <sub>GE</sub>	Internal gate-emitter resistance		20		kΩ
f <sub>ASIC</sub>	ASIC system switching frequency		8		MHz
td(on) <sub>IO</sub>	Input-output turn-on propagation time	0,3	0,45	0,6	μs
td(off) <sub>IO</sub>	Input-output turn-off propagation time	0,3	0,45	0,6	μs
t <sub>d(err)</sub>	Error input-output propagation time	1,15	1,3	1,5	μs
tpERRRESET	Error memory reset time	7	15	27	μs
t <sub>TD</sub>	Interlock dead time adjustable	no interlock		4,1	μs
V <sub>CEstat</sub>	Reference voltage for V <sub>CE</sub> -monitoring		5,8		V
t <sub>blank</sub>	Blanking time		3,5		μs
C <sub>ps</sub>	Coupling capacitance primary-secondary		40		pF
MTBF	Mean Time Between Failure T <sub>a</sub> = 40°C		1		10 <sup>6</sup> h
w	weight		95		g
HxBxT	Dimensions		20x57x114		mm

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